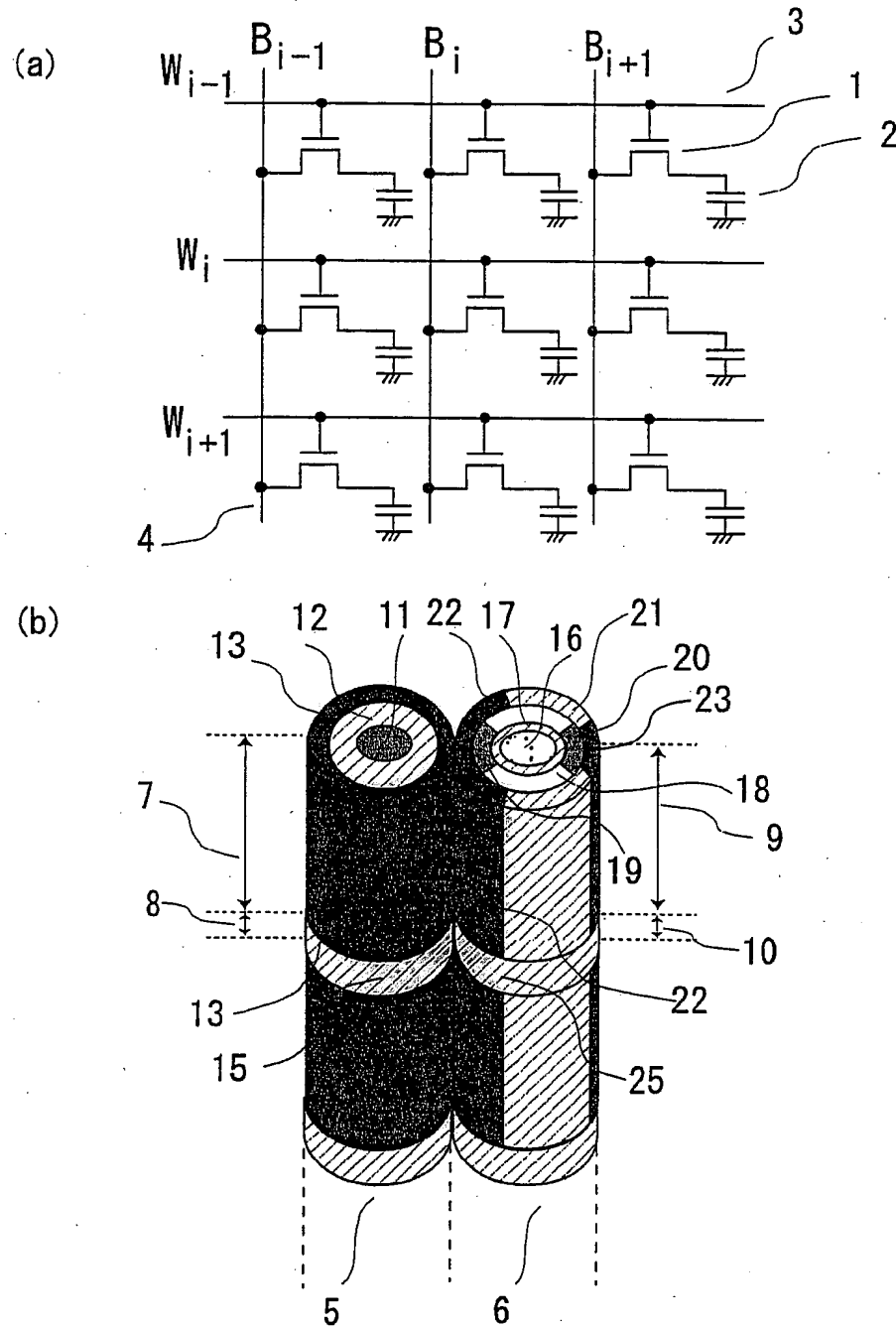


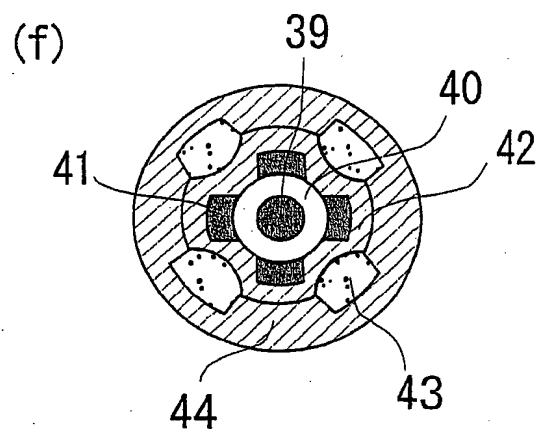
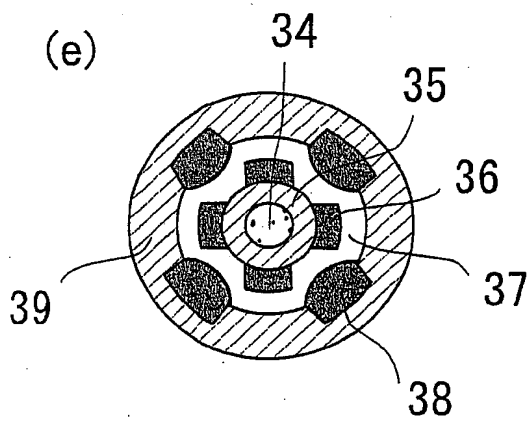
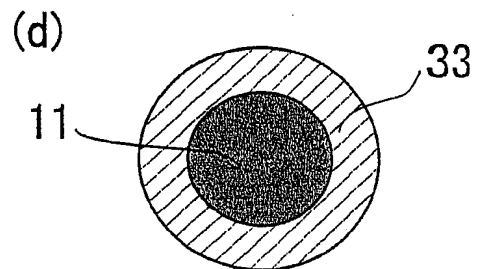
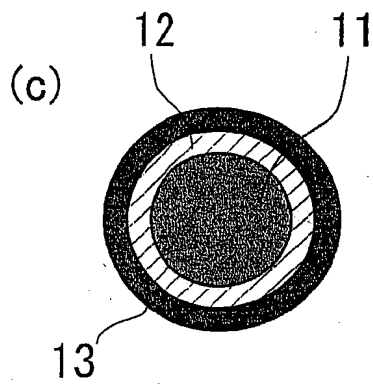
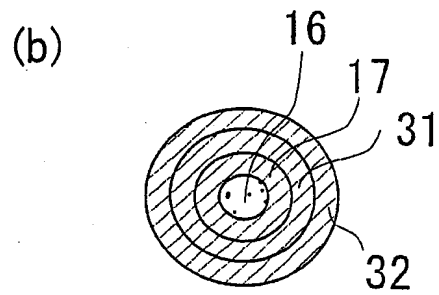
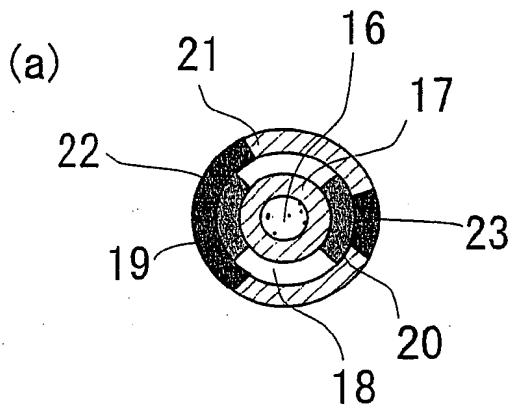
1/28

[図1]



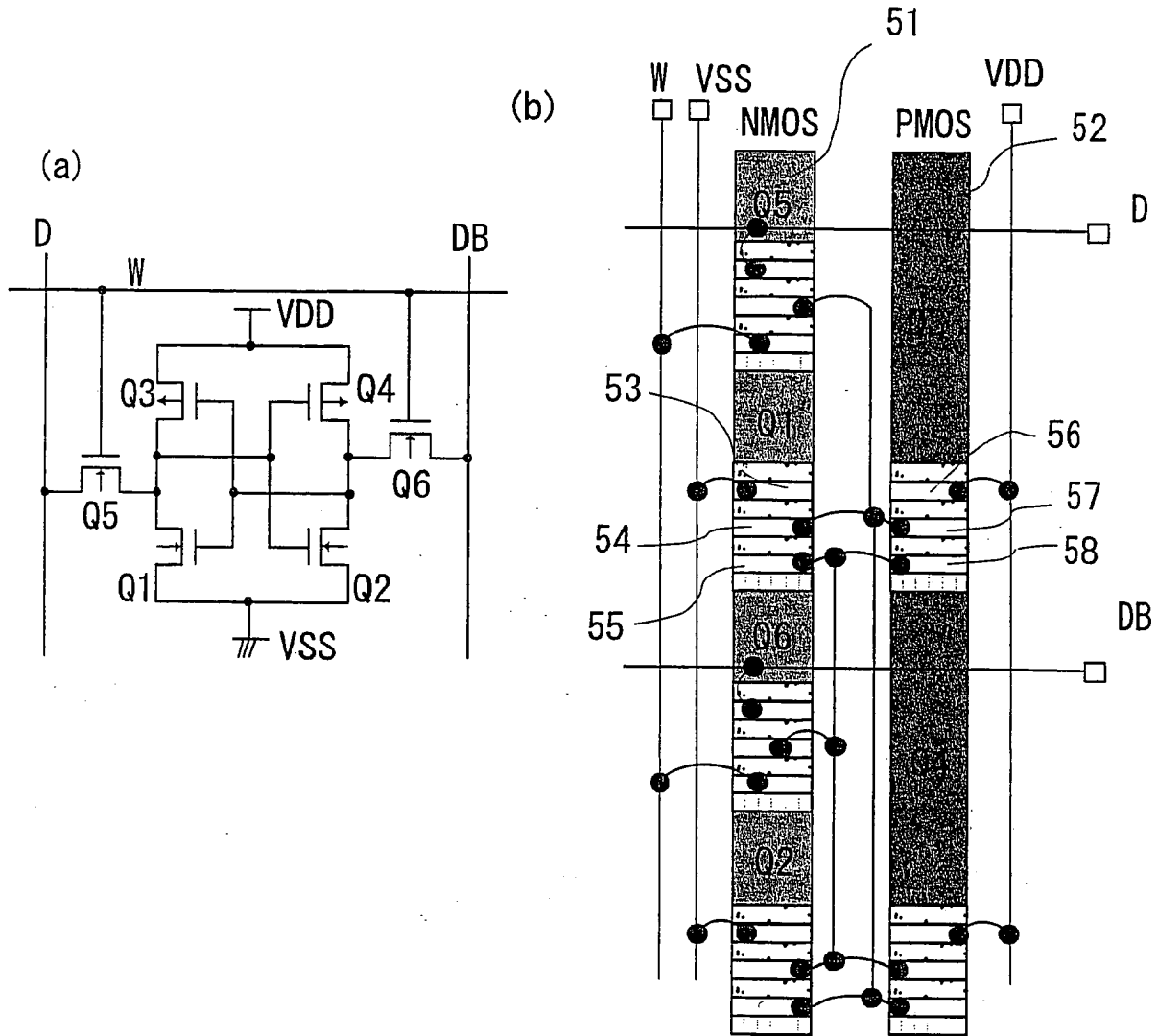
2/28

[図2]



3/28

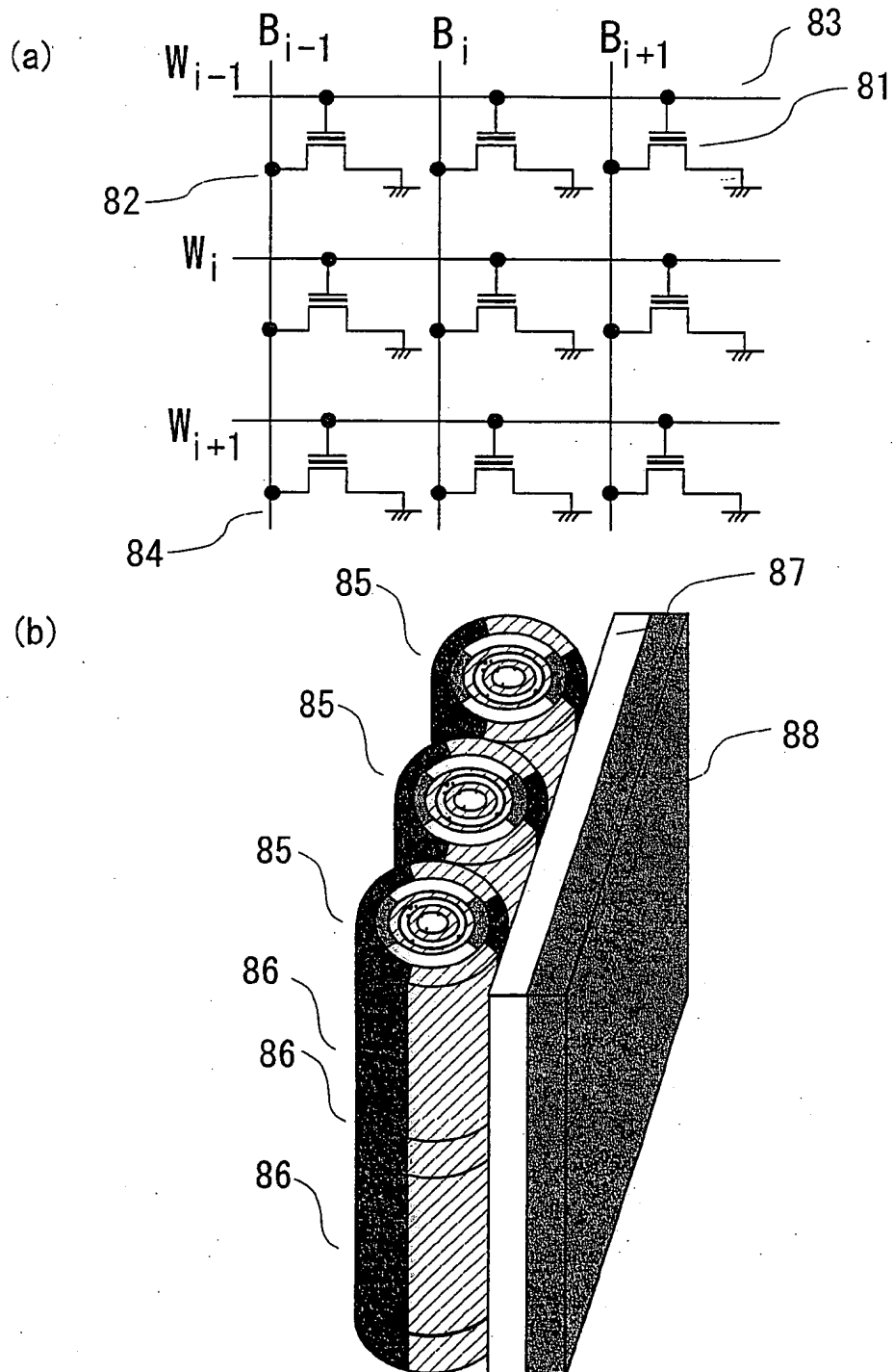
[圖3]





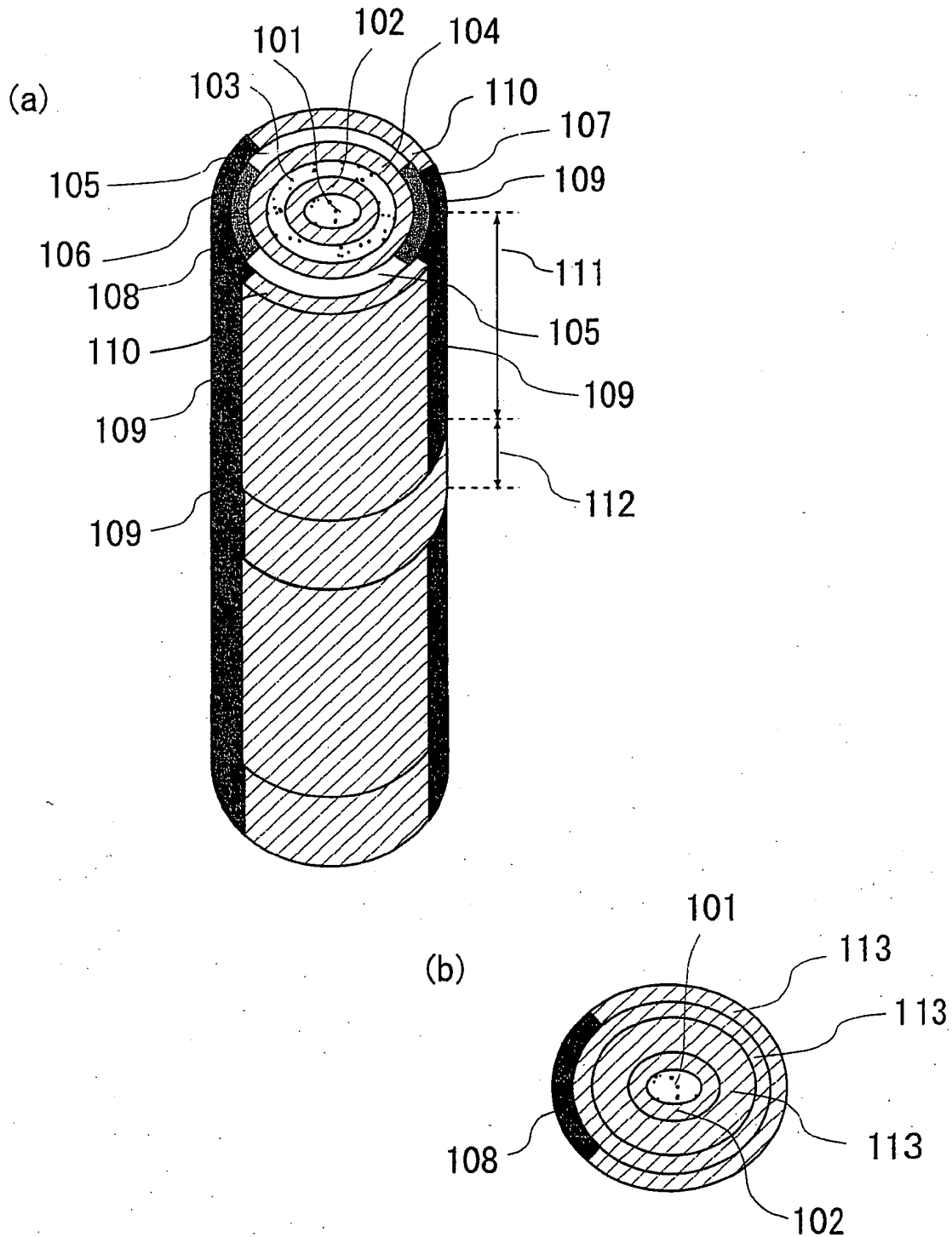
5/28

[5]



6/28

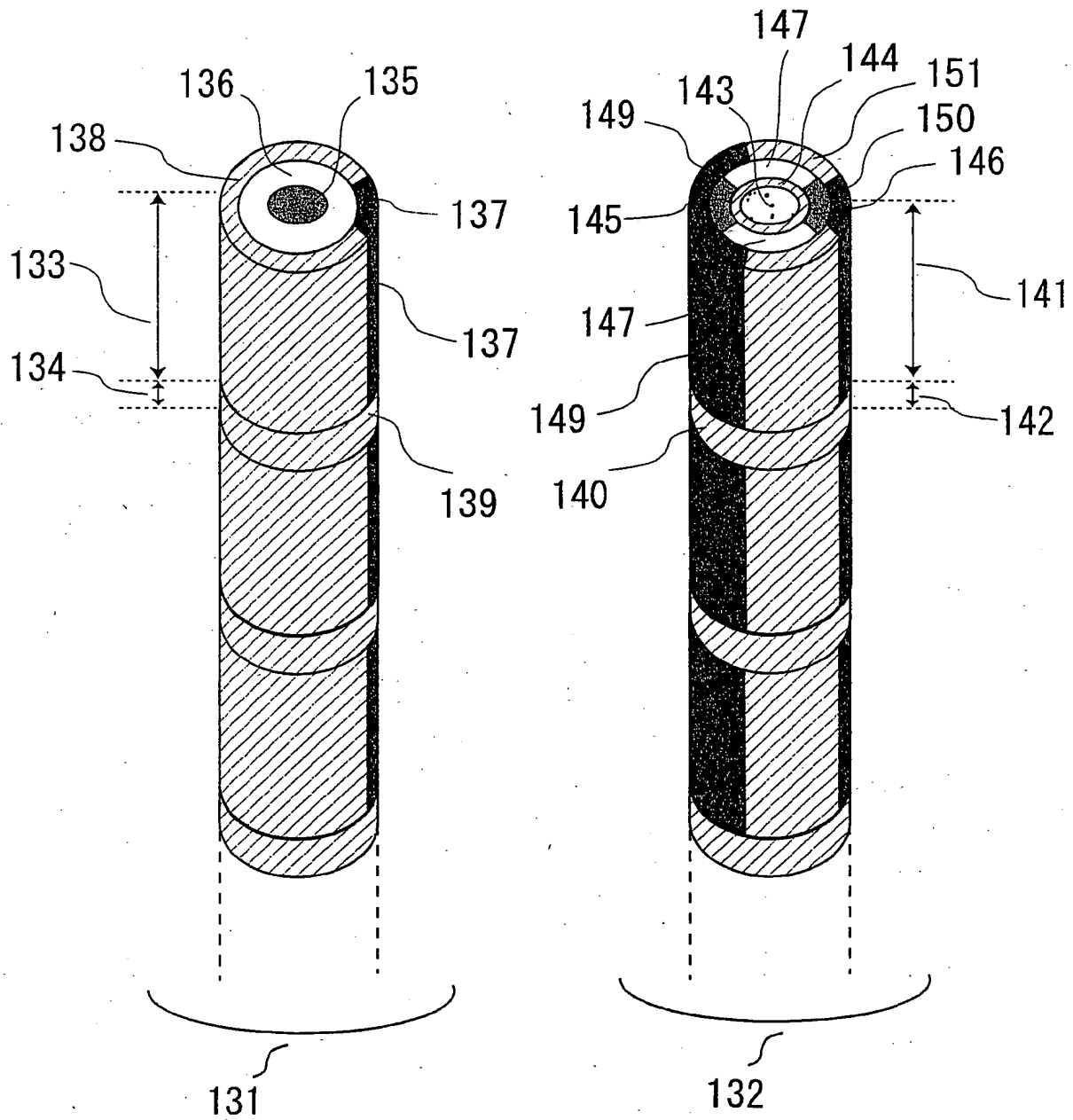
[圖6]



The diagram shows a 4x4 array of photodiodes (123) connected to a readout circuit. A vertical bus (121) is connected to the cathodes of the photodiodes in each column. The anodes are connected to a grid of horizontal lines (125) and vertical lines (126). A horizontal bus (122) is connected to the top of the array. The output of the array is connected to an inverter (128) and a signal line (129).

8/28

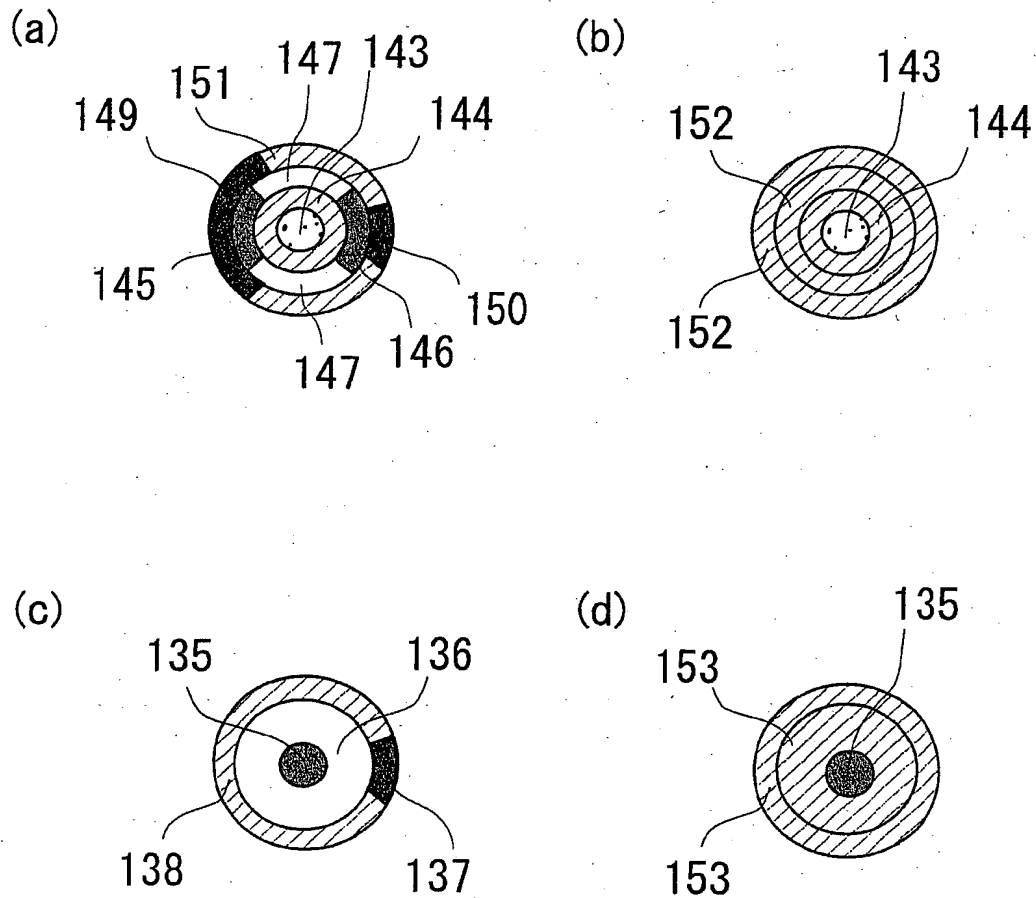
[図8]





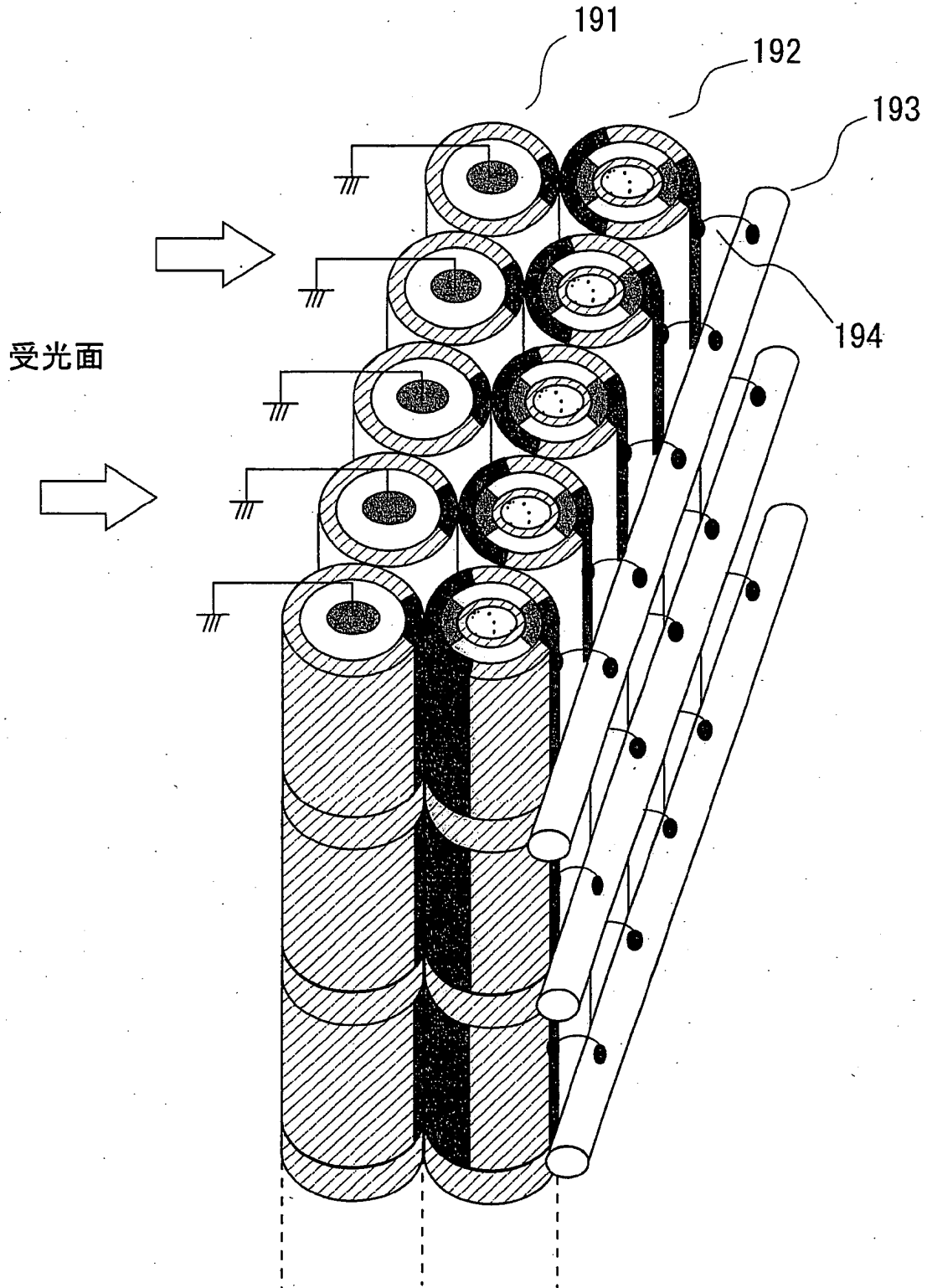
9/28

[9]



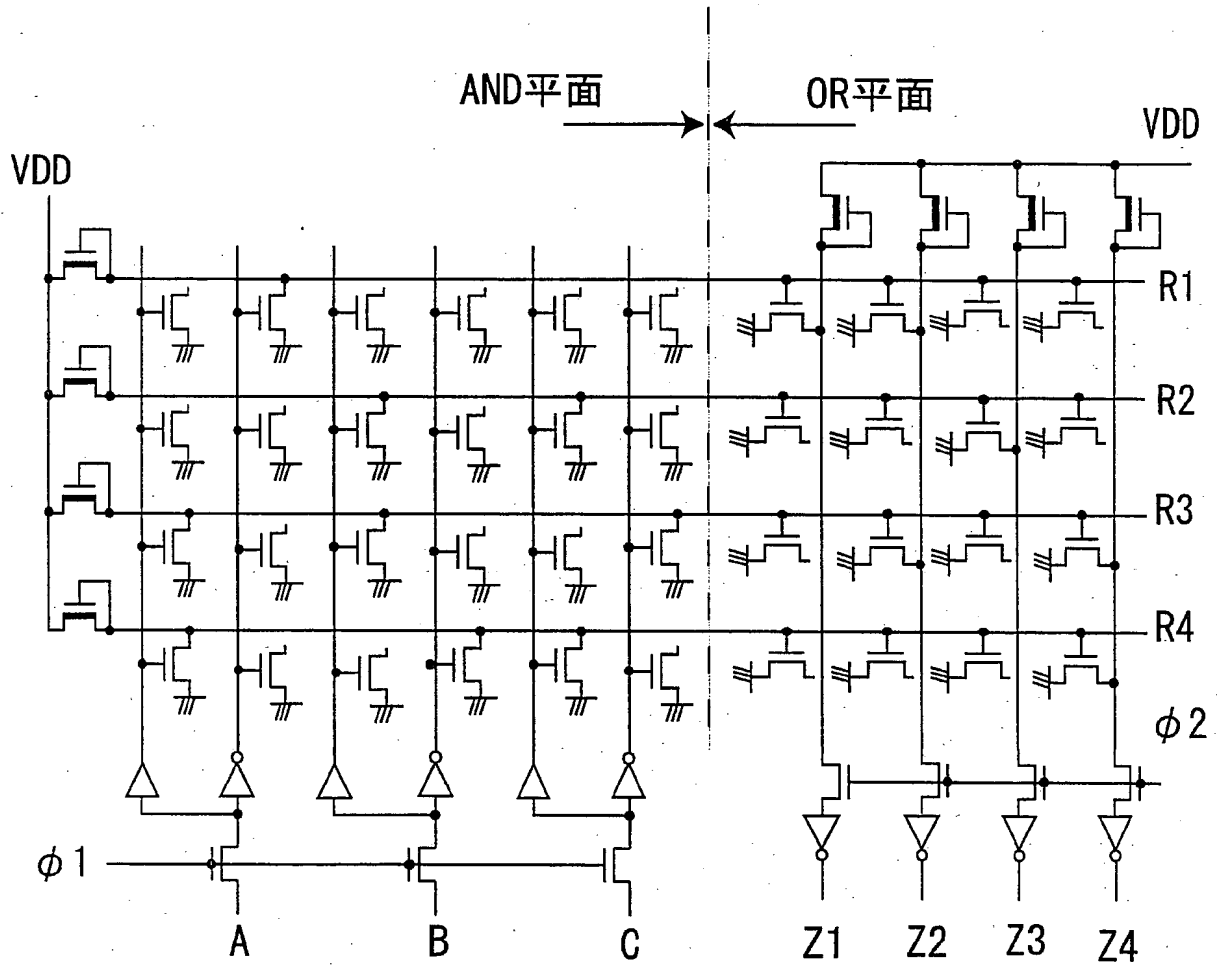
10/28

[図10]



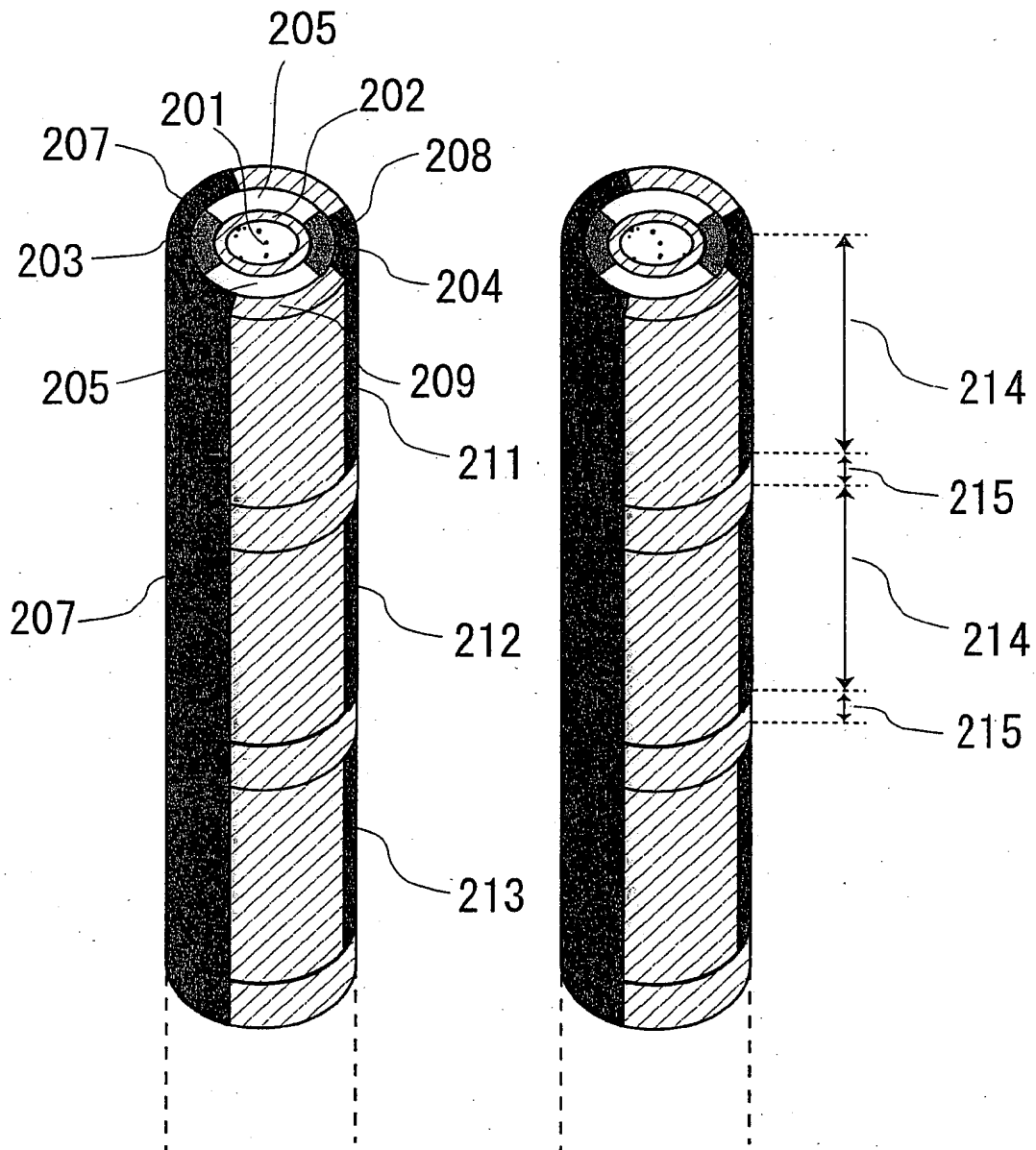
11/28

[図11]

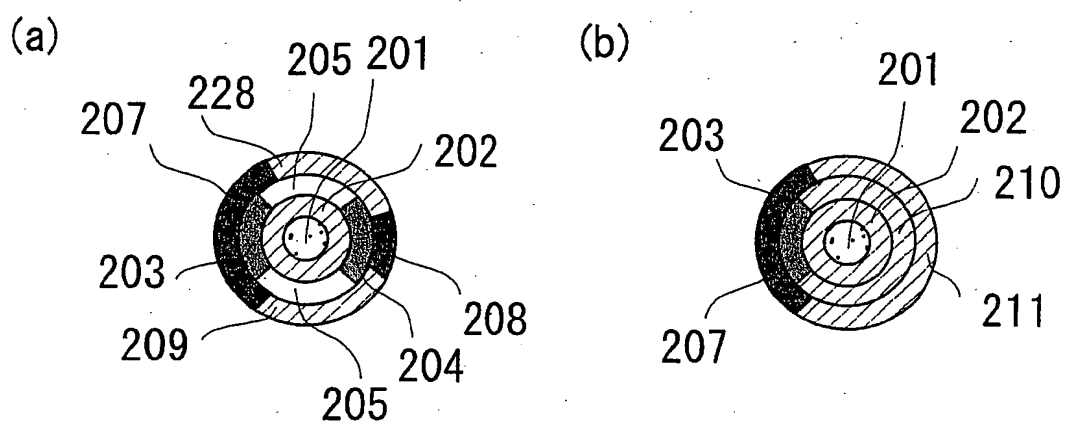


12/28

[FIG. 12]

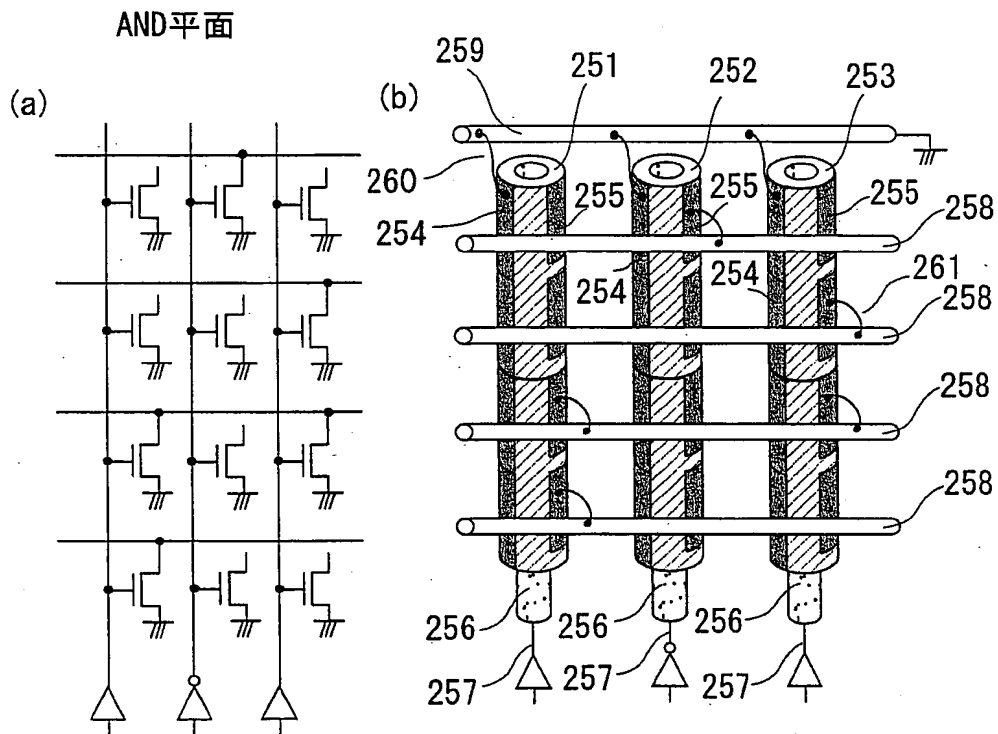


[FIG. 13]



14/28

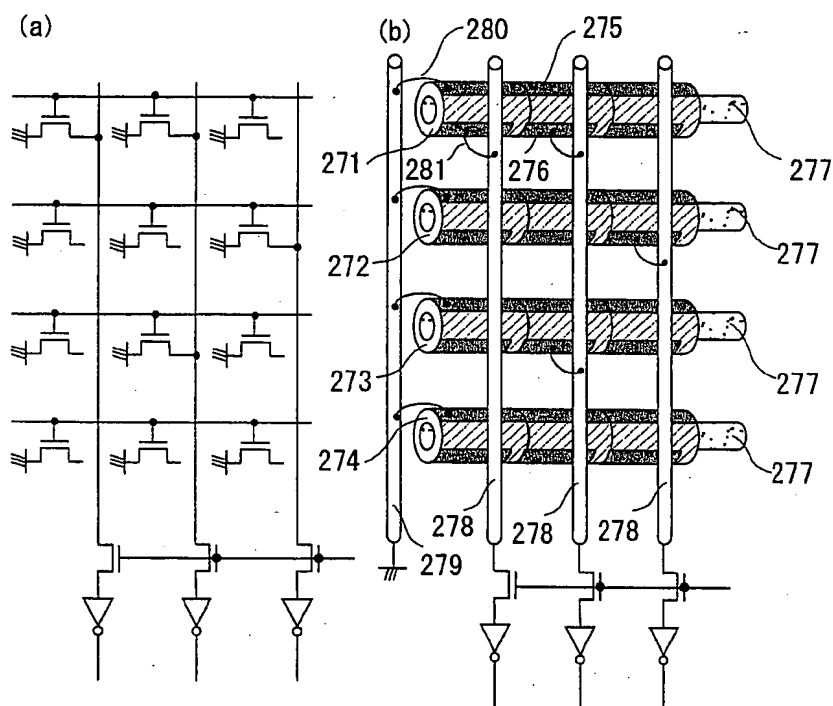
[図14]



15/28

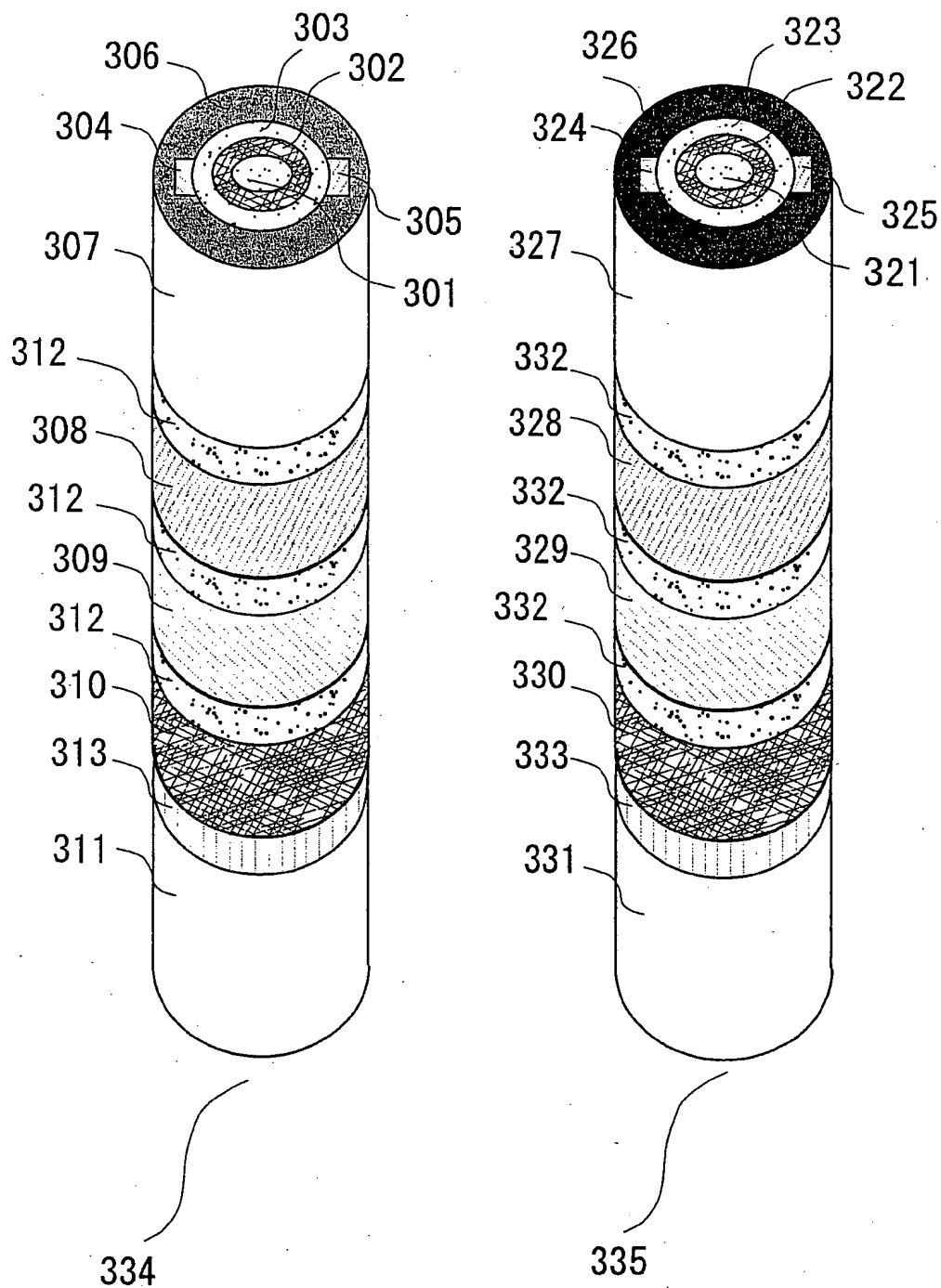
[図15]

OR平面



16/28

[図16]





(a)

301

302

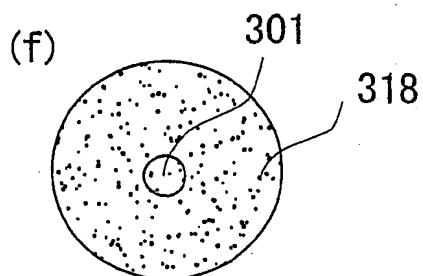
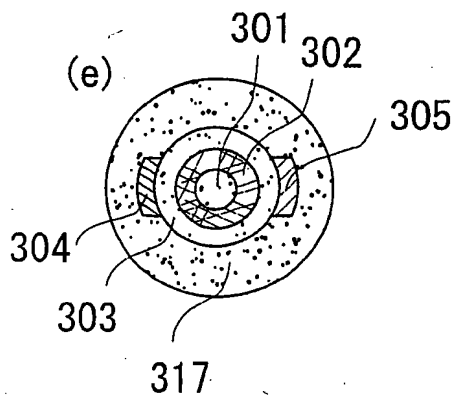
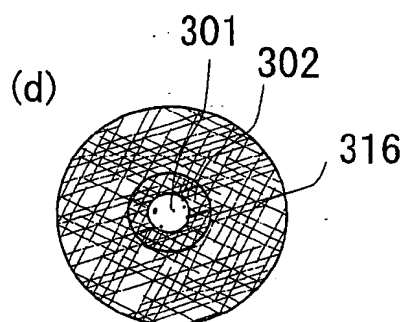
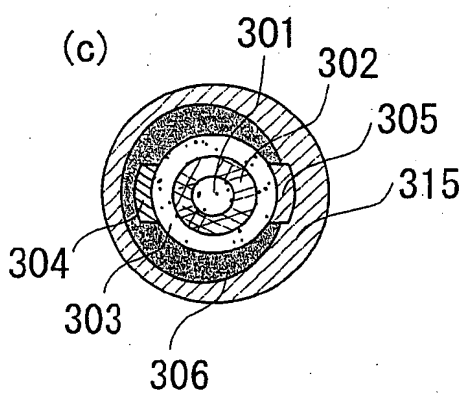
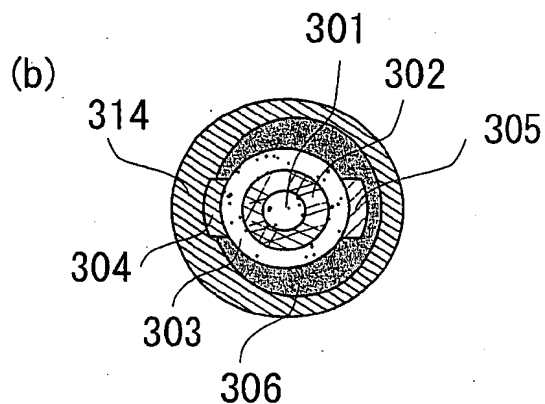
305

304

303

306

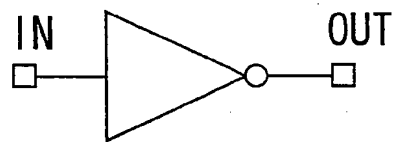
Detailed description: This diagram shows a cross-section of a circular device. It features a central core (301) surrounded by a ring (302). A layer (303) contains two hatched rectangular blocks (304) and a series of small dots. An outer ring (305) is also shown. A line (306) points to the bottom edge of the device.



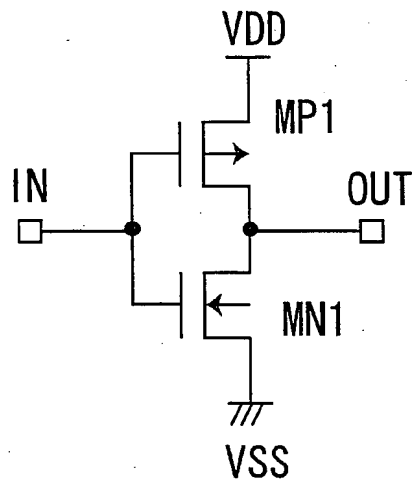
18/28

[18]

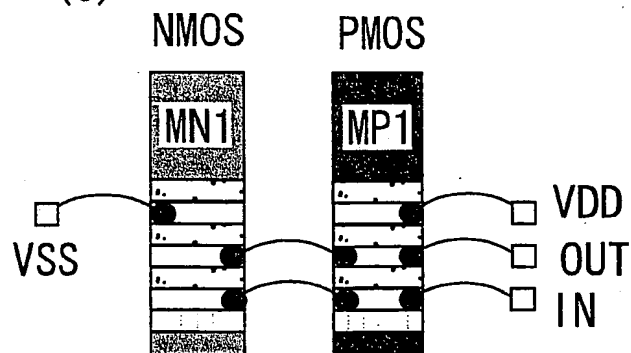
(a)



(b)



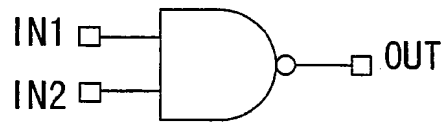
(c)



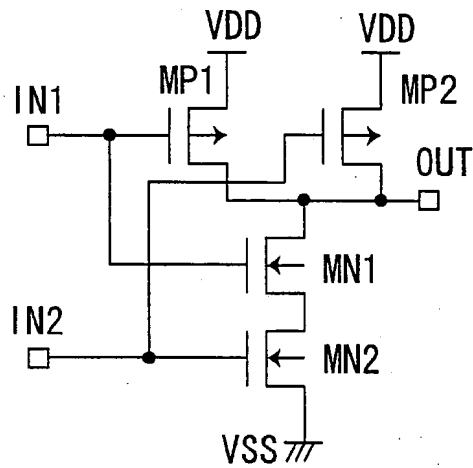
19/28

[19]

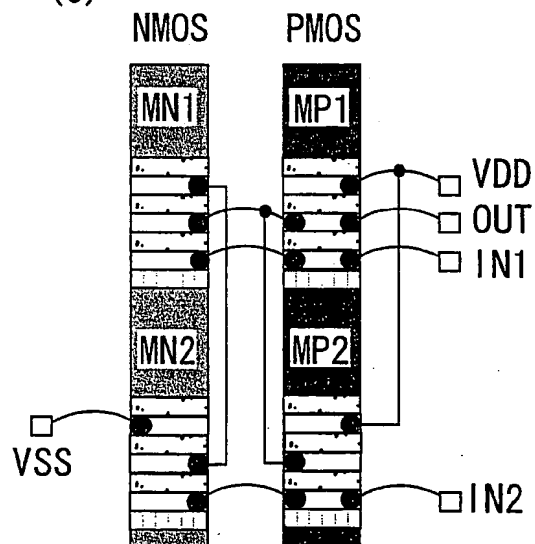
(a)



(b)

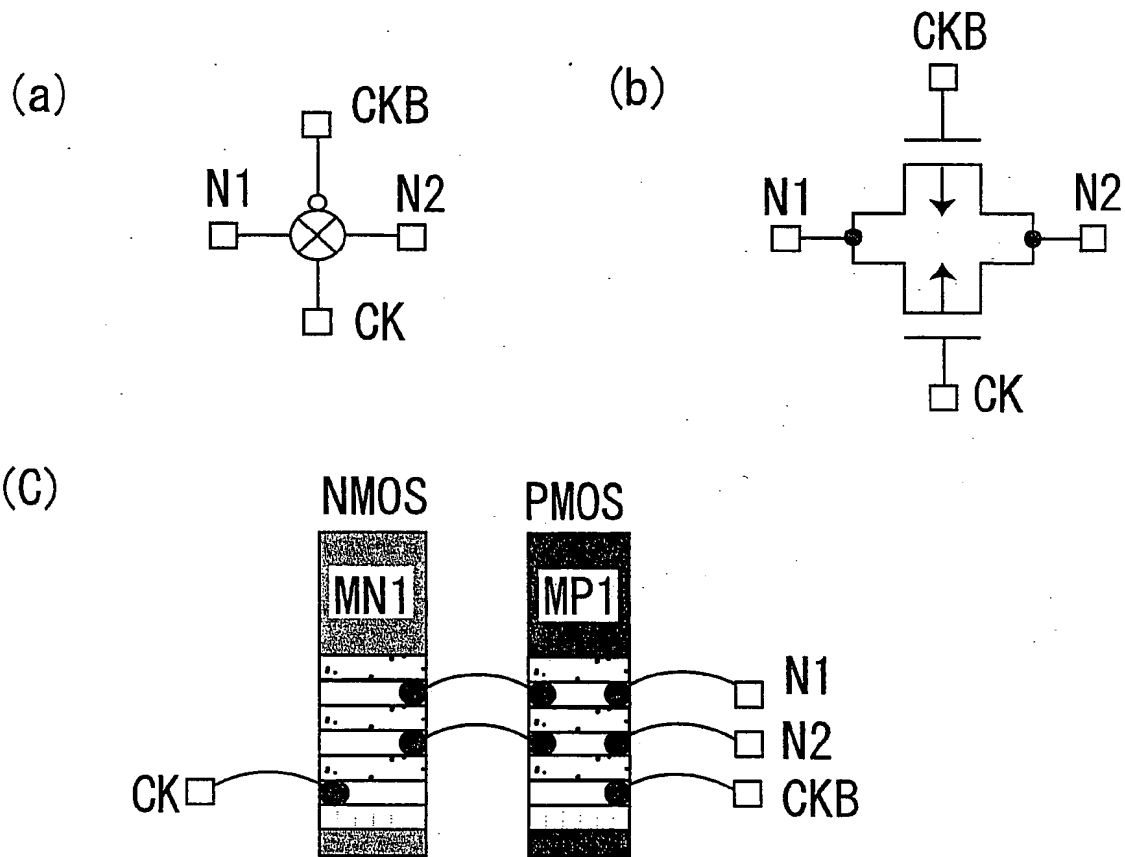


(c)



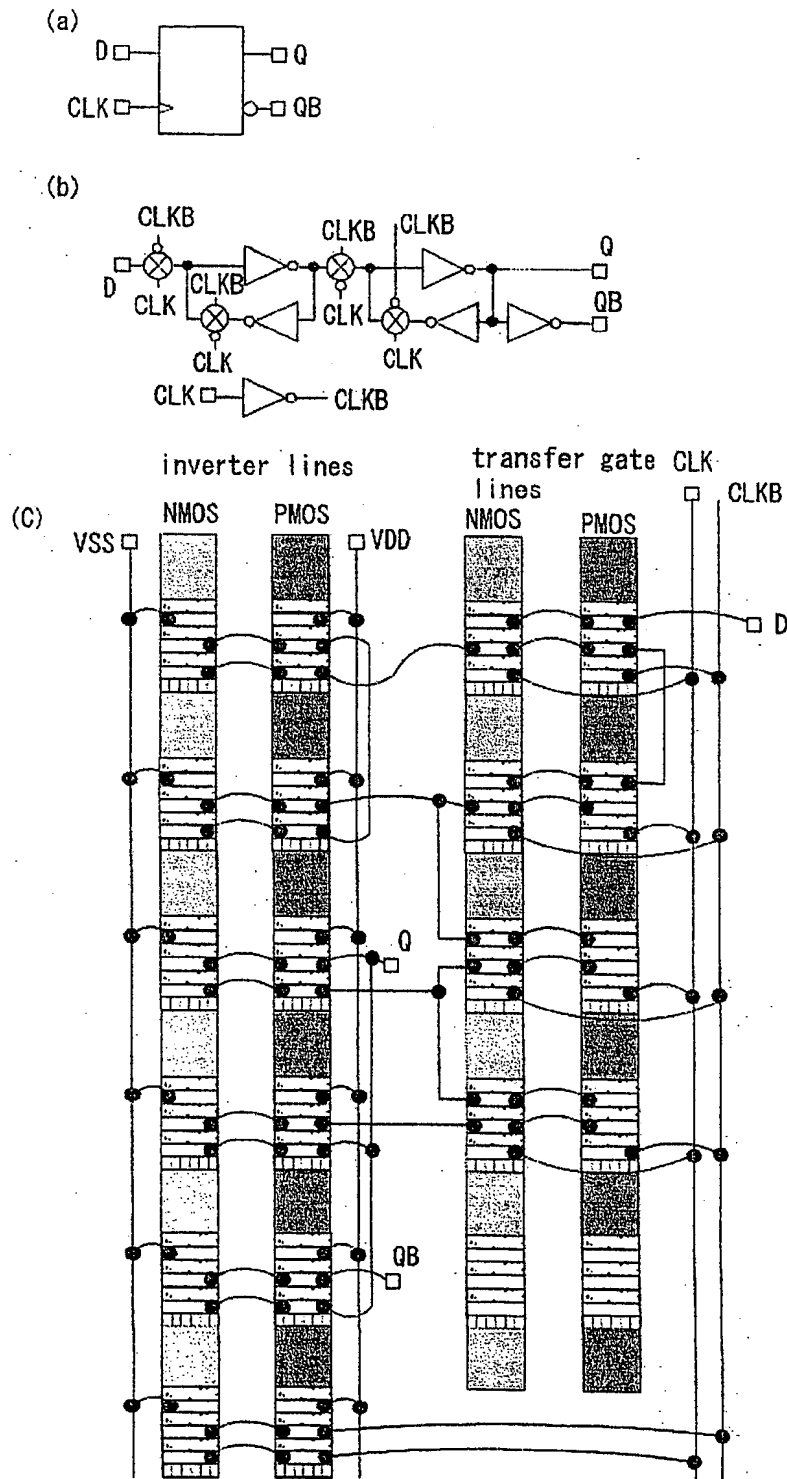
20/28

[20]



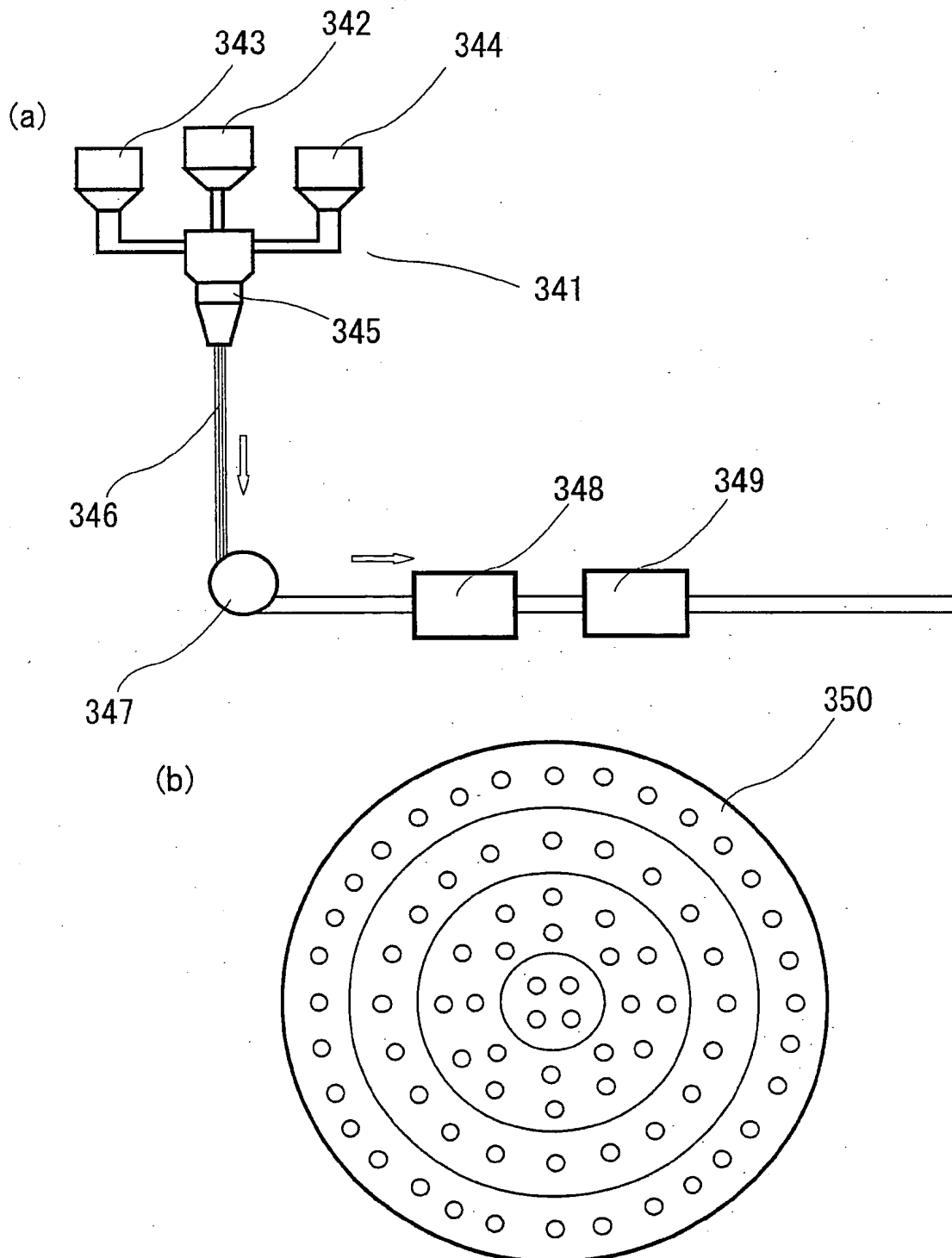
21/28

[21]



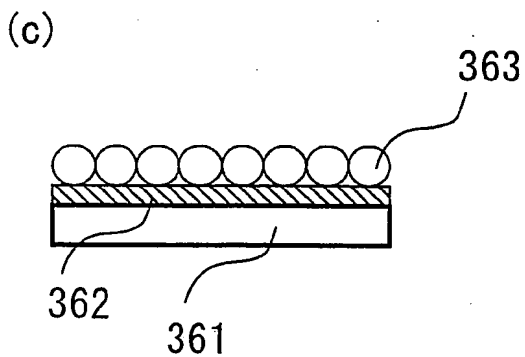
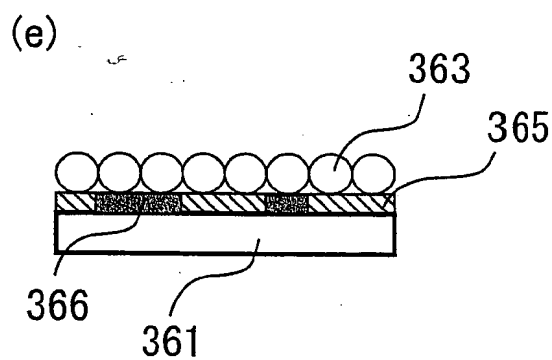
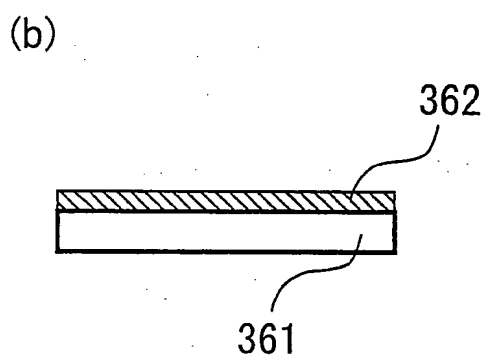
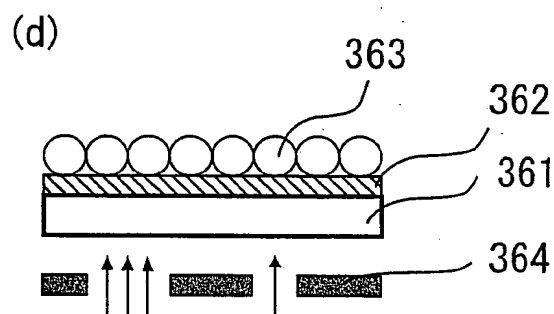
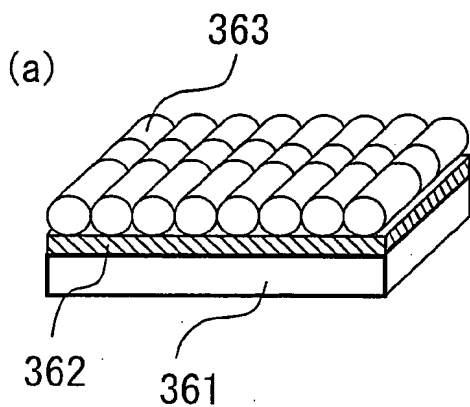
22/28

[ 22]



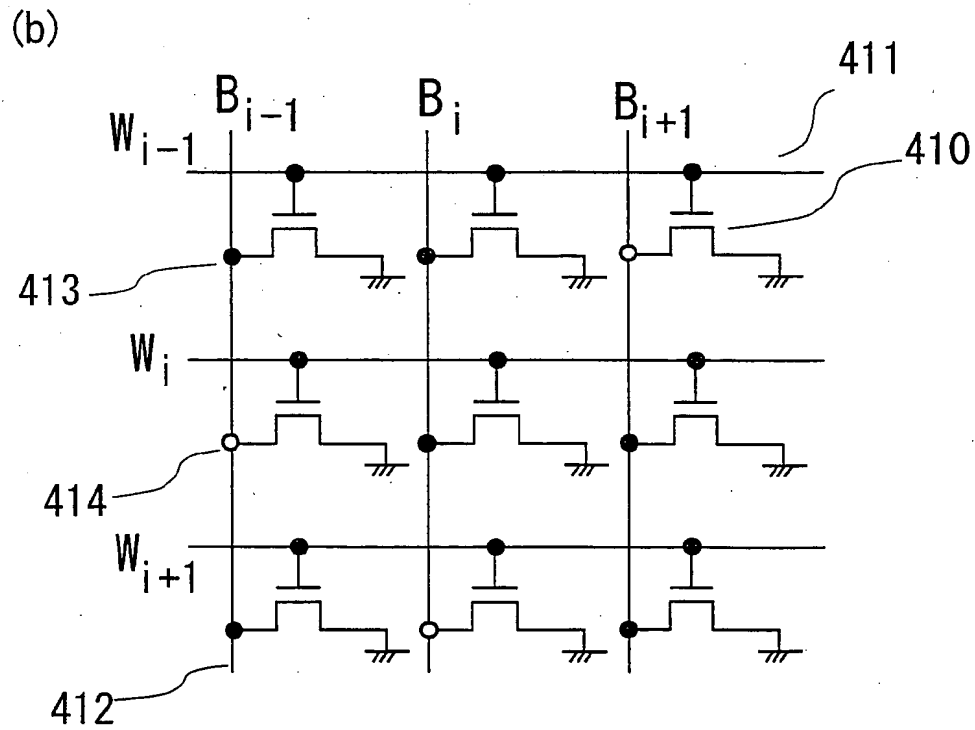
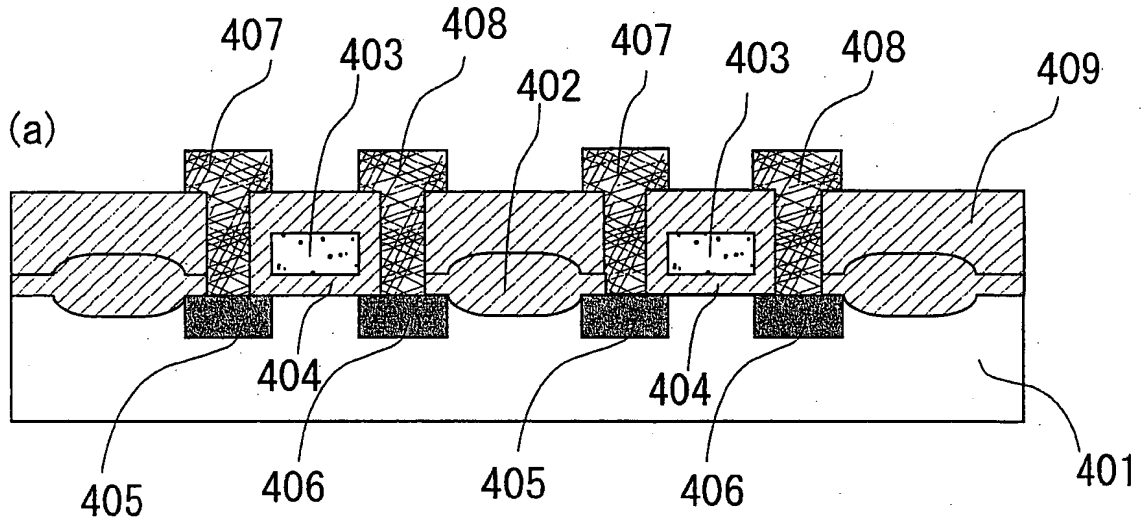
23/28

[ 23]



24/28

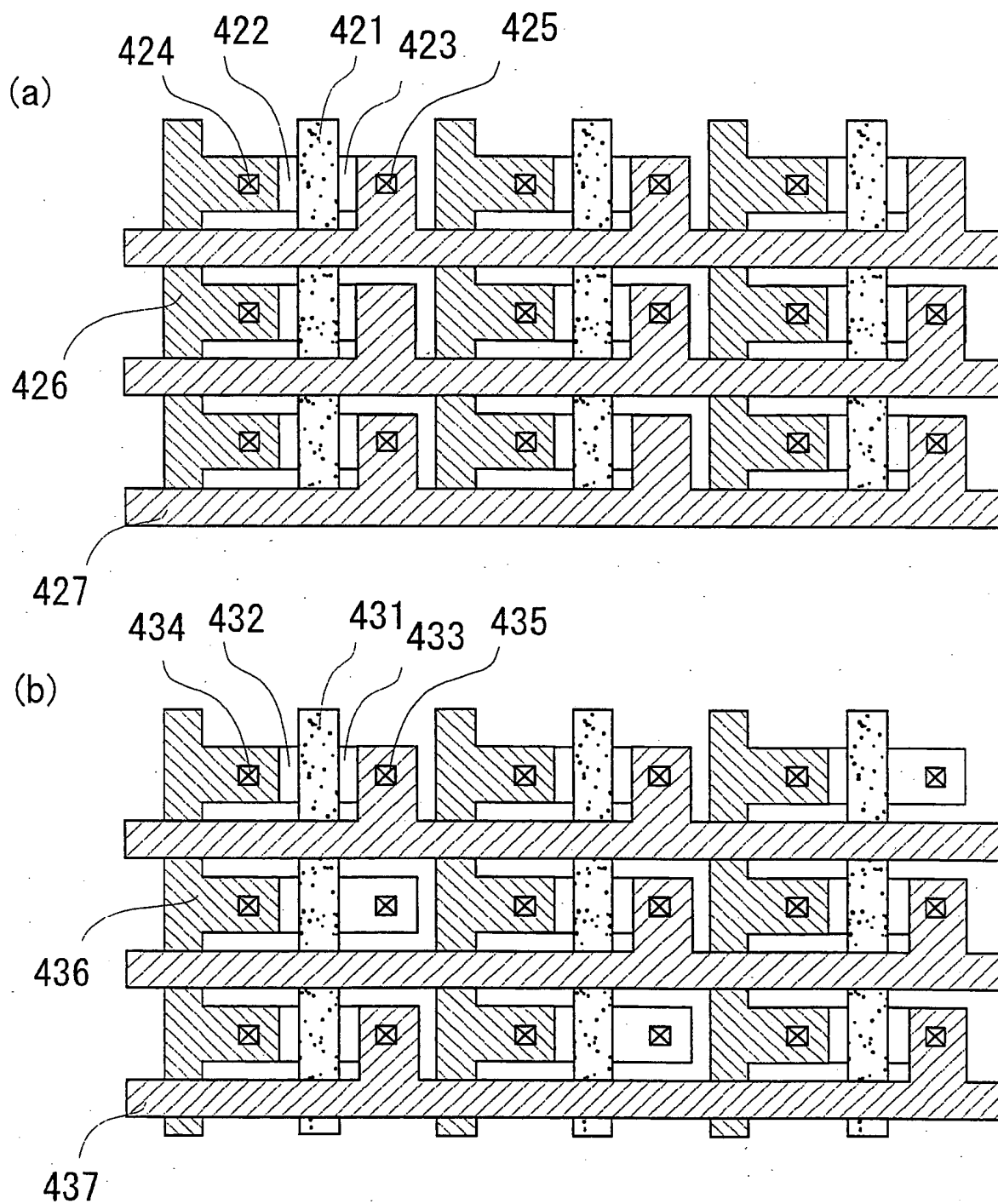
[図24]





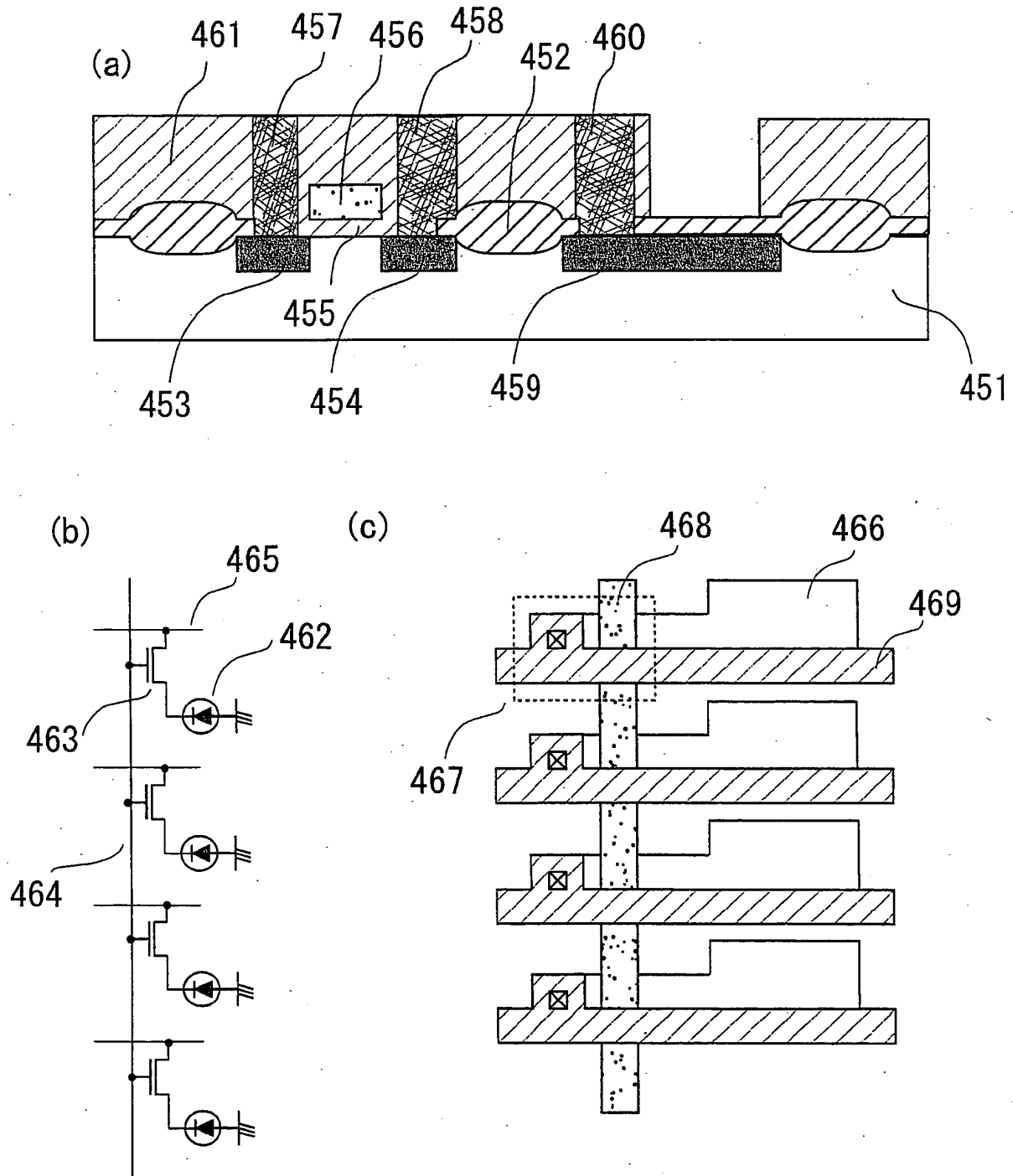
25/28

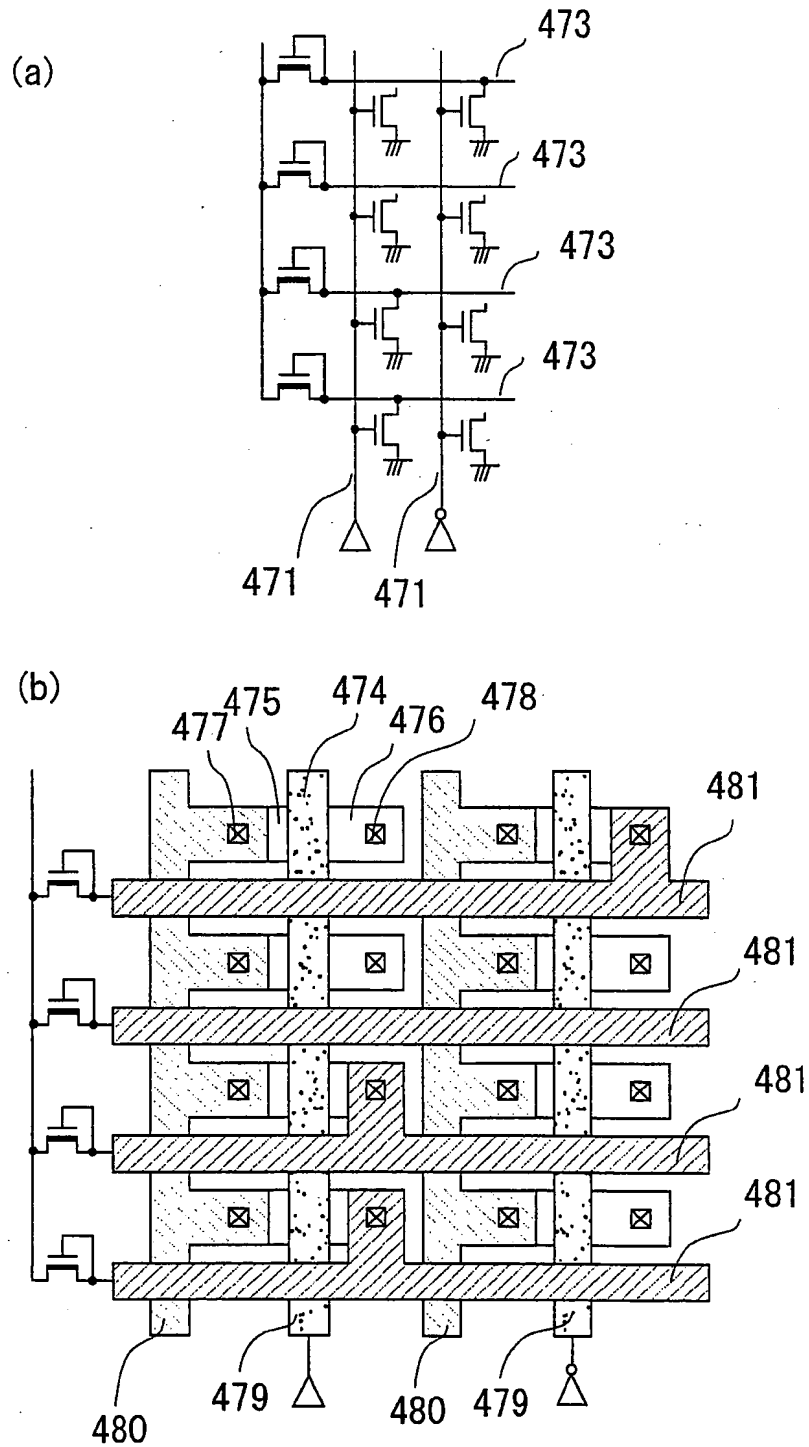
[ 25]



26/28

[26]





28/28

[ 28]

